Please amend the present application as follows:

In the Specification

The following is a copy of portions of the specification that identifies language being added with underlining ("_____") and language being deleted with strikethrough ("-----"), as is applicable:

Please replace the two consecutive paragraphs beginning on page 11, line 4, and ending on page 12, line 13 with the following amended paragraph:

The input stage 352 includes transistors 310, 312, 318, 320, and 326. The input node 302 is connected with the gate of the switching or input transistor 320. The ~~source~~ drain of the switching transistor 320 is connected with the drain of a charging transistor 312. The gate and the drain of the charging transistor 312 are connected, The source of the charging transistor 312 is connected with the supply voltage. The ~~drain~~ source of the switching transistor 320 is connected with the ~~source~~ drain of the current sink transistor 326. The ~~drain~~ source of the current sink transistor 326 connected with ground. The gate of the current sink transistor 326 is connected with a Bias signal at an input node 330. The Bias signal is provided by a bandgap circuit and is a reference current. The ~~drain~~ source of the charging transistor 310 is connected with the supply voltage, and the gate and ~~source~~ drain of the charging transistor 310 are interconnected. The drain of the complementary transistor 318 is connected with the ~~source~~ drain of the charging transistor 310 in a cascode configuration. The complementary transistor 318 turns-on when transistor 320 turns-off and vice versa. The source of the complementary transistor 318 is connected with the drain of the transistor 326 in a cascode configuration. The gate of the complementary transistor 318 is connected with a voltage divider circuit that includes several series connected resistors 332 - 346. The gate voltage of the complementary transistor 318 is maintained at approximately half of the supply voltage as a function of the resistors 332 – 346. The signal, Vb, at node 356 is determined by the voltage divider and is preferably approximately half of the supply voltage. The signal, Vb, is connected with the gate of the complementary transistors 318 and 324. Thus, the output signal is isolated from the switching noise at the switching transistor 320.

The input stage 354 includes several transistors 314, 316, 322, 324, and 328. The input node 304 is connected with the gate of the switching or input transistor 322. The switching transistor 322 receives the input signal "NDW." The drain of the switching transistor 322 is connected with the ~~source~~ drain of the charging transistor 314 in a cascode configuration. The gate and ~~source~~ drain of the charging transistor 314 are interconnected, and the ~~drain~~ source of charging transistor 314 is connected with the supply voltage. The source of the switching transistor 322 is connected with the drain of the sink transistor 328. The source of the sink transistor 328 is connected with ground. The gate of the sink transistor 328 is connected with the Bias signal at input node 330. The ~~drain~~ source of the charging transistor 316 is connected with the supply voltage 350. The gate of the charging transistor 316 is connected with the ~~source~~ drain of the charging transistor 310. The ~~source~~ drain of the charging transistor 316 is connected with the drain of the complementary transistor 324 in a cascode configuration. The complementary transistor 324 turns-on when the switching transistor 322 turns-off and vice versa. The sources of the transistors 322 and 324 are connected with the drain of the sink transistor 328. The gate of the complementary transistor 324 is connected with the voltage divider that includes resistors 332 - 346, such that the gate voltage of the complementary transistor 324 is approximately half of the supply voltage.